

Your Partner in Innovation Embedded Solutions



VEST HMI Development Kits

VEST, a leading provider of embedded solutions, offers a comprehensive suite of Human Machine Interface (HMI) Development Kits designed to accelerate customer product development. Choice of Panels; 12.1", 15.6" and 21.5" capacitive touchscreen display.





High Brightness / Sunlight Readable



Rich Multimedia





ABOUTOUR **PRODUCT**

VEST HMI (Human Machine Interface) Development Kits empower developers to create cutting-edge HMIs leveraging the robust NXP i.MX8M processors (i.MX8M Plus and i.MX8M Mini). These high-performance processors, featuring the ARM® A53 Core, deliver exceptional multimedia capabilities, enabling stunning 3D and 2D graphics on displays ranging from compact 4-inch panels to large 4K displays via HDMI. With extensive experience designing for harsh operating environments, VEST ensures its products meet stringent IP65/67 (water/dust) and IK08 (impact resistance) standards for touch panels, along with other critical requirements, including sunlight readability. VEST also provides customized graphical user interface (GUI) design and software UI/application development services using Qt, React JS, Android, and LVGL.

Prepare for the future with our forthcoming Next Generation Contactless HMI, integrating microphone arrays with Voice Intelligent Technology powered by deep learning speech recognition and mmWave 60GHz radar sensing.

VEST HMI Development Kits are ideally suited for a wide range of applications, including:

- Industrial Automation Displays
- Video/Audio Conferencing
- Advanced Human Machine Interface Applications
- Point of Sale, Digital Signage, Smart Retail, and Smart City Solutions
- Point of Care and Medical Devices
- Test and Measurement Instruments (including portable formats)
- Industry 4.0 Automation

Key Features

- Rich multimedia capabilities, including dual display support with the i.MX8M Plus
- Integrated 10W audio amplifier with plug-and-play speaker driver
- Support for key industrial protocols, including NXP Real Time Edge software (i.MX8M Plus), OPC-UA, Modbus, and CAN Bus
- Versatile camera interface via MIPI CSI, supporting a range of resolutions.
- Interface for future-ready contactless HMI functionalities, including voice intelligence technology and mmWave (60GHz) sensing

Support

ADVANCED PRODUCTS CORPORATION PTE LTD (APC)

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Website: <u>apc-vest.com</u> Email: <u>sales@apc-vest.com</u> E-Store: <u>shop.apc-vest.com</u> LinkedIn: <u>www.linkedin.com/company/advanced-product-corporation</u>





	VEST i.MX8M Mini Dev Kit (NXP iMX8M Mini)	VEST i.MX8M Plus Dev Kit (NXP iMX8M Plus)
CPU Details		
CPU	Up to 4x Cortex-A53 @ 1.8GHz, Cortex-M4 @ 400 MHz	Up to 4x Cortex-A53 @ 1.8GHz, Cortex-M7 @ 800 MH:
GPU	GC Nano Ultra for 3D Acceleration, GC 320 for 2D Acceleration	16 GLOPS (high precision) OpenGL ES 3.1/3.0, Vulkan, Open CL™ 1.2FP, Open VG™ 1.1
NPU		NPU operating at up to 2.3 TOPS
Memory		
Memory	2GB 32-bit LPDDR4-3000	2GB 32-bit LPDDR4-3000
Storage	16GB eMMC 5.1	16GB eMMC 5.1
External Storage	Micro SD 3.0 Socket Push-Push	Micro SD 3.0 Socket Push-Push
Operating System/Drive	r	
BSP	Yocto Linux, Debian, and Android	Yocto Linux, Debian, and Android
Driver	Omni Vision OV5640 camera	NXP Real-Time Edge Software LI-IMX715-MIPI Camera, Optimom 2.0, Optimom 1.5
Multimedia		
Video Encoder	1080p60, H.264, VP8	1080p60, H.264 / H.265
Video Decoder	1080p60, H.264 / H.265, VP9, VP8	1080p60 HEVC, H.264 / H.265, VP9, VP8
Camera	1x MIPI CSI	2x MIPI CSI (4 lane each), 2x ISP
Audio	Headphone Jack with microphone input, header for Speaker L&R up to 10W / channel into 8 ohm load	
Display and Touch	LVDS connector with backlight for LCD Panel, I2C Touch Connector	LVDS connector with backlight for LCD Panel, I2C Touch Connector, HDMI 2.0a TX
Panel		
Size	12.1", 15.6", 21.5"	12.1", 15.6", 21.5"
Technology	TFT	TFT
Resolution	1280(W) x 800(H), 1920(W) x 1080(H) [for 15.6" & 21.5"]	1280(W) x 800(H), 1920(W) x 1080(H) [for 15.6" & 21.5
Luminance	1200, 900, 500 nits	1200, 900, 500 nits
View Angle (H/V)	160 / 160 , 170 / 170, 178 / 178	160 / 160, 170 / 170, 178 / 178
Multi-Touch Point	10	10
Connectivity		
Wireless	Dual Band WiFi 802.11a/b/g/n/ac 2x2 MIMO + Bluetooth 5.2	
Networking	10/100/1000 BaseT RJ45 Ethernet with PoE	1x 10/100/1000 BaseT RJ45 Ethernet with PoE,
		1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support
USB	1x USB2.0/3.0 Type C with PD, 2x US	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and
	,,	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support
Serial Communication	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default c	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO)
Serial Communication I/O Expansion	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default o PCIe M.2 Key E 2230 (1 Lane PCIe 6	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART)
Serial Communication I/O Expansion Debugging & Programming	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default o PCIe M.2 Key E 2230 (1 Lane PCIe 2x Debug-UART Header, 2.45mm Pitch 5pin	1x 10/100/1000 BaseT RJ45 Ethemet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2 x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO)
Serial Communication	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default o PCIe M.2 Key E 2230 (1 Lane PCIe 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header
Serial Communication I/O Expansion Debugging & Programming Buttons and Indicators	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default o PCIe M.2 Key E 2230 (1 Lane PCIe 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2 2x 12C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x 12C, 2x UART) Gen3.0, USB, SDIO, 12S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation
Serial Communication I/O Expansion Debugging & Programming Buttons and Indicators Power	R\$485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default of PCIe M.2 Key E 2230 (1 Lane PCIe of 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re PoE (25W/chant	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2 2x 12C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x 12C, 2x UART) Gen3.0, USB, SDIO, 12S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation
Serial Communication I/O Expansion Debugging & Programming Buttons and Indicators Power Physical	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default of PCIe M.2 Key E 2230 (1 Lane PCIe d 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re PoE (25W/chann 180mm x 120mm (ind	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation hel), USB-C (60w)
Buttons and Indicators Power Physical Form Factor	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default of PCIe M.2 Key E 2230 (1 Lane PCIe d 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re PoE (25W/chann 180mm x 120mm (ind	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation mel), USB-C (60w)
Serial Communication I/O Expansion Debugging & Programming Buttons and Indicators Power Physical Form Factor Operating Temperature	RS485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default of PCIe M.2 Key E 2230 (1 Lane PCIe d 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re PoE (25W/chann 180mm x 120mm (ind	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART : 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation nel), USB-C (60w)
Serial Communication I/O Expansion Debugging & Programming Buttons and Indicators Power Physical Form Factor Operating Temperature Ordering Information	R\$485 with 120 ohm Termination (de M.2 KeyB (4-lane 1x MIPI CSI < M.2 KeyB (LVDS 4/8 Lane default of PCIe M.2 Key E 2230 (1 Lane PCIe d 2x Debug-UART Header, 2.45mm Pitch 5pin 1x Power Button, 1x Force Recovery, 1x System Re PoE (25W/chant 180mm x 120mm (ind Commercial / Ind	1x 10/100/1000 BaseT RJ45 Ethernet with PoE and TSN Support SB 2.0/3.0 Type A, 1x USB 2.0 Type A efault) or RS232, 2x CAN FD, 3x UART 2x I2C, 2x UART, 2x SPI, GPIO) or MIPI DSI (4 Iane), 2x I2C, 2x UART) Gen3.0, USB, SDIO, I2S, UART, GPIO) header, JTAG-1.27mm Pitch 2x5 Pin Header set, LEDs for PoE operation, LEDs for USB Operation hel), USB-C (60w) cluding SMARC SOM) dustrial (optional)

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